



Quarra PTP Ethernet Switch

RAVENNA、AES67に対応した、高精度時間同期プロトコル IEEE 1588v2 (PTPv2) 機能搭載イーサネットスイッチ

Quarra 1G Compact PTP Ethernet Switch (10port , 1/2 Rack)



Quarra 1G PTP Ethernet Switch (10port , 1U Rack)



Quarra 10G PTP Ethernet Switch (28port , 1U Rack)



製品特徴

- RAVENNA、AES67、SMPTE ST2110 対応
- ナノ秒単位の正確な高精度時間同期プロトコル IEEE 1588v2 (PTPv2)対応
- Synchronous(L1) Ethernet (同期イーサネット)の正確なクロック同期にDSPLL®技術を使用(オプション)
- Web GUI または、コマンドラインにより、すばやく簡単に設定可能
- QoS、IGMP v2/3、VLAN機能搭載
- RJ45ポートは全ポート 1 G、SFP+対応 (10portモデル 2.5G / 28portモデル 10G)

SPECIFICATIONS

Carrier Ethernet

Layer-2 IEEE 1588v2 : Precision Time Protocol (PTP) timing and synchronization
 IEEE 802.1ad : Provider bridging (VLAN Q-in-Q) switch - 8 MACs, 4K VLANs
 Per queue MEF E-LINE or per port MEF E-LAN, E-TREE Service Points
 - Per port per queue Dual Leaky Bucket Service Policers with PCP or DSCP remarking per Service port
 - Static tagging options per Service Point
 OAM Hardware for generating CCM messages, CCM checking is done by software
 - Software of OAM and protection switching

Quality of Service

EPS and ERPS Protection Switching
 TCAM-based VLAN classification and translation with pattern matching against Layer-2 through Layer-4 information
 Up to 256 QoS and VLAN TCAM entries
 Eight QoS queues per port with strict or deficit weighted round-robin scheduling (DWRR)
 DSCP translation, both ingress and/or egress
 DSCP remarking based on QoS and VLAN TCAM entries
 VLAN (PCP, DEI and VID) translation, both ingress and egress
 Per-queue, per-port policing and shaping, programmable in steps of 100 kbps
 Per-flow policing through TCAM-based pattern matching, Up to 256 policers
 Full-duplex flow control (IEEE 802.3x) and half-duplex backpressure, symmetric and asymmetric

Layer 2 Switch

Port mirroring
 Port-based access control
 IEEE 802.3ad : Link aggregation
 IEEE 802.1x : Network Access Control
 IEEE 802.1w : Rapid Spanning Tree Protocol
 IEEE 802.1s : Multiple Spanning Tree Protocol
 Simple Network Management Protocol (SNMP)
 Independent and shared VLAN learning
 256 VLAN egress tagging TCAM entries
 Link aggregation traffic distribution is programmable and based on Layer-2 through Layer-4 information
 Basic Switching - Forwarding, address learning and address aging
 VLAN IEEE 802.1Q (4,096 VLAN's) - Push / pop / translate up to two VLAN tags; translation in ingress and / or egress
 8,192 MAC addresses. Including Generic Attribute Registration Protocol (GARP)
 Wire-speed hardware-based learning and CPU-based learning configurable per port

Multicast

8K Layer-2 Multicast group addresses with 64 port masks
 8K IPv4/IPv6 multicast groups
 Internet Group Management Protocol v2 & v3
 Multicast Listener Discovery (MLDv1)
 Multicast Listener Discovery (MLDv2) with source specific multicast forwarding

Physical, Environmental and Electrical - 1G QUARRA Compact Unit

Electrical rating	100-240Vac, 0.3-0.2A, 50-60Hz, 50Watts (Maximum)
Maximum Temp	0-40C
Relative Humidity	10 to 95% (non-condensing)
Dimensions (h, w, d)	1.75" x 8.75" x 7.875" (44.45mm x 222.25mm x 200mm)
Weight	3.0lbs. (1.4kg.)

Physical, Environmental and Electrical - 1G QUARRA Rackmount 1RU

Electrical rating	100-240Vac, 0.3-0.2A, 50-60Hz, 50Watts (Maximum)
Maximum Temp	0-40C
Relative Humidity	10 to 95% (non-condensing)
Dimensions (h, w, d)	1.75" x 17.25" x 6.75" (44.45mm x 438.15mm x 171.45mm)
Weight	5.5lbs. (2.5kg.)

Physical, Environmental and Electrical - 10G QUARRA Rackmount 1RU

Electrical rating	100-240Vac, 0.4-0.2A, 50-60Hz, 50Watts (Maximum)
Maximum Temp	0-40C
Relative Humidity	10 to 95% (non-condensing)
Dimensions (h, w, d)	1.75" x 17.25" x 8.0" (44.45mm x 438.15mm x 203.2mm)
Weight	7.0lbs. (3.2kg.)



RAVENNA AES67



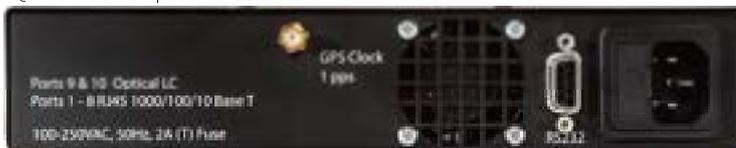
SMPTE Standards-based Timing

ST 2110-10 System Timing and Definition

ST 2059-2 SMPTE Profile for Use of IEEE-1588 Precision Time Protocol

REAR PANEL

QUARRA 1G Compact PTP Ethernet Switch



QUARRA 1G PTP Ethernet Switch



QUARRA 10G PTP Ethernet Switch



この製品に関するお問い合わせ先



有限会社Digicom

〒454-0012

愛知県名古屋市中川区尾頭橋3-11-15

TEL 052-324-8385

FAX 052-324-8386